

REMARKS

Claims 6-24 are pending in the present application. Claims 1-5 have been canceled and Claims 6-24 have been added to claim subject matter included in the application. Reconsideration of the pending Claims is respectfully requested in view of the amendments to the Claims and the following remarks.

Objections to the Specification

Applicant has amended the title as suggested in the office action mailed April 5, 2006. Thus, Applicant respectfully requests withdrawal of the objection to the specification.

The 35 U.S.C. §101 rejection

Claim 5 was rejected pursuant to 35 U.S.C. §101 as being directed to non-statutory subject matter. Claim 5 has been canceled rendering this rejection moot.

The 35 U.S.C. §112 second paragraph rejection

Claim 4 was rejected pursuant to 35 U.S.C. §112 second paragraph as being indefinite due to the limitation "said writing means." Claim 4 has been canceled rendering this rejection moot.

The Double Patenting Rejection

Applicant respectfully requests that the provisional double patenting rejection be reconsidered in view of the cancellation of Claim 1.

Claim Rejections pursuant to 35 U.S.C. §102(e) and 35 U.S.C. §103(a)

Claims 1-2 and 5 were rejected pursuant to 35 U.S.C. §102(e) as being anticipated by U.S. Patent Publication No. US 2004/0078636 A1 to Suzuki. (hereinafter "Suzaki"). In addition, Claim 3 was rejected pursuant to 35 U.S.C. §103(a) as being obvious in view of Suzaki in combination with U.S. Patent Publication No. 2004/0111443 to Wong et al. (hereinafter "Wong"). Further Claim 4 was rejected pursuant to 35 U.S.C. §103(a) as being obvious in view of Suzaki, Wong and U.S. Patent Publication No. US 2003/0014496 A1 to Spencer et al. (hereinafter "Spencer").

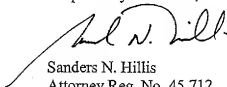
Applicant respectfully traverses these rejections since each and every limitation included in new Claims 6-24 are not taught, suggested, or disclosed by the cited prior art, either alone or in combination. For example, Claim 6 describes a processor operable, when said contents are

received, to write said contents in said cache memory means, and said processor is further operable, after said contents are stored in said cache memory means, to process or execute said contents automatically, absent receipt of a command initiated by a user. On the other hand, neither Suzakai, Wong or Spencer, either alone or in combination describe a processor operable to receive and store contents, and to process or execute the contents automatically absent receipt of a command initiated by a user.

In another example, Claim 6 describes that the processor is further operable, in response to a command receivable via said operation input means to store said contents that have been processed or executed by said processor, to read said contents from said cache memory means, and to write said contents in said content storage means. Again, none of the cited prior art either alone or in combination describe such a read and write operation in response to a command received via an operation input means. The claims dependent from Claim 6, and Claims 11-24 also describe limitations that are not taught, suggested, or disclosed by the cited prior art either alone or in combination.

With this amendment and response, Applicant believes that the present pending claims of this application are allowable, and respectfully requests the Examiner to issue a Notice of Allowance for this application. Should the Examiner deem a telephone conference to be beneficial in expediting allowance/examination of this application, the Examiner is invited to call the undersigned attorney at the telephone number listed below.

Respectfully submitted,



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